PATENT APPLICATION

Pocket Number: 040008-0307459 Client Reference: OG03-028

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Rothe Application of

Chee Hong CHOI, et al.

Group Art Unit: 2812

Application No.: 10/747,618

Examiner: Unassigned

Filed: December 30, 2003

Confirmation No.: 2872

For: METHOD FOR FABRICATING TRANSISTOR OF SEMICONDUCTOR DEVICE

## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Applicants respectfully request the Examiner return an initialed copy of the enclosed Form PTO-1449 to Applicants with the next Office communication to indicate that the reference(s) has been considered, per MPEP § 609.

This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required. English-language Abstracts of the non-English language references are attached hereto.

Respectfully Submitted,

Glenn T. Barrett

Registration Number 38705 Customer Number: 00909

Date: April 29, 2004

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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation in conformance and not considered. Include copy of this form with next communication to Applicant.	